

Substitute for Form 1449/PTO <div style="display: flex; align-items: center; justify-content: center;"> <div> <h2 style="margin: 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="margin: 0; font-size: small;">(use as many sheets as necessary)</p> </div> </div>				Complete if Known	
		Application Number	10/728,608		
		Filing Date	December 5, 2004		
		First Named Inventor:	Robert S. Chau et al.		
		Art Unit	Unknown- 2818		
		Examiner Name	Unknown Dao Nguyen		
Sheet 1 of 2		Attorney Docket Number	42P14705D		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
DN		US-	6,207,977 B1	3/27/2001	Augusto	
DN		US-	5,683,934	11/4/1997	Candelaria	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code ³	Number ³ Kind Code ³ (if known)				
DN		JP	60-251666	5/2/1986	Suwa Seikosha KK		
DN		EP	0 531 550 A1	3/17/1993	Asahi Chemical Ltd.		
DN		EP	1 020 898 A	7/19/2000	Matsushita Electric Inc. Co.		
DN		WO	01/88995 A	11/22/2001	Phillips Timothy Jonathan		
DN		EP	1 119 029 A	7/25/2001	Fujitsu Ltd		

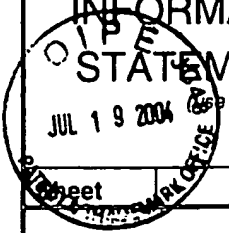
Examiner Signature	/Dao Nguyen/	Date Considered	06/30/2006
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Sheet	2	of	2			



NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
DN		International Search Report PCT/US 03/34667	
DN		Ikeda, K. et al. "50-NM Gate Schottky Source/Drain P-MOSFETS with a SIGE Channel" IEEE Electron Device Letters, IEEE Inc., New York, US, Vol. 23, No. 11, November 2002, pages 670-672, XP001158217, ISSN: 0741-3106, Figure 1	
DN		Chang, C-Y et al. "High Performance Thin-Film Transistors with Low-High-Low Band Gap Engineering", Proceeding of the SPIE, Bellingham, VA, US, Vol. 3421, July 1998, pages 152-158, XP001189049, ISSN: 0277-786X	

Examiner Signature	/Dao Nguyen/	Date Considered	06/30/2006
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